

Figure 1

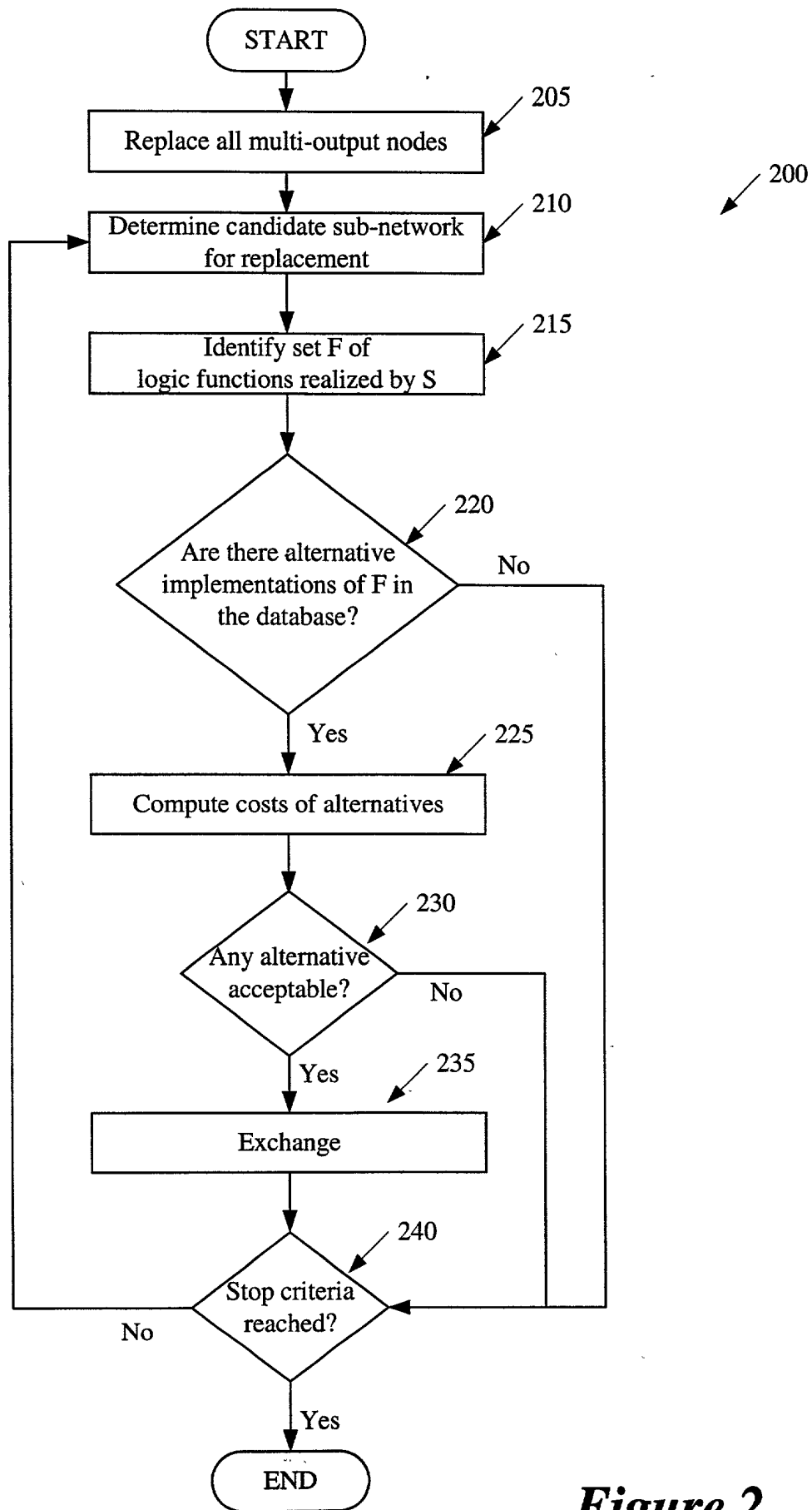


Figure 2

FIG. 3 is a block diagram of a logic circuit 300. The circuit 300 includes a first AND gate 310, a second AND gate 320, a third AND gate 325, a fourth AND gate 330, a fifth AND gate 305, a sixth AND gate 315, a seventh AND gate 335, and an eighth AND gate 340. The circuit 300 also includes a first OR gate 345, a second OR gate 350, a third OR gate 355, a fourth OR gate 360, a fifth OR gate 365, a sixth OR gate 370, a seventh OR gate 375, and an eighth OR gate 380. The circuit 300 further includes a first input 385, a second input 390, a third input 395, a fourth input 400, a fifth input 405, a sixth input 410, a seventh input 415, an eighth input 420, a ninth input 425, a tenth input 430, an eleventh input 435, a twelfth input 440, a thirteenth input 445, a fourteenth input 450, a fifteenth input 455, a sixteenth input 460, a seventeenth input 465, an eighteenth input 470, a nineteenth input 475, a twentieth input 480, a twenty-first input 485, a twenty-second input 490, a twenty-third input 495, a twenty-fourth input 500, a twenty-fifth input 505, a twenty-sixth input 510, a twenty-seventh input 515, a twenty-eighth input 520, a twenty-ninth input 525, a thirtieth input 530, a thirty-first input 535, a thirty-second input 540, a thirty-third input 545, a thirty-fourth input 550, a thirty-fifth input 555, a thirty-sixth input 560, a thirty-seventh input 565, a thirty-eighth input 570, a thirty-ninth input 575, a fortieth input 580, a forty-first input 585, a forty-second input 590, a forty-third input 595, a forty-fourth input 600, a forty-fifth input 605, a forty-sixth input 610, a forty-seventh input 615, a forty-eighth input 620, a forty-ninth input 625, a fiftieth input 630, a fifty-first input 635, a fifty-second input 640, a fifty-third input 645, a fifty-fourth input 650, a fifty-fifth input 655, a fifty-sixth input 660, a fifty-seventh input 665, a fifty-eighth input 670, a fifty-ninth input 675, a sixtieth input 680, a sixty-first input 685, a sixty-second input 690, a sixty-third input 695, a sixty-fourth input 700, a sixty-fifth input 705, a sixty-sixth input 710, a sixty-seventh input 715, a sixty-eighth input 720, a sixty-ninth input 725, a seventieth input 730, a seventy-first input 735, a seventy-second input 740, a seventy-third input 745, a seventy-fourth input 750, a seventy-fifth input 755, a seventy-sixth input 760, a seventy-seventh input 765, a seventy-eighth input 770, a seventy-ninth input 775, an eightieth input 780, an eighty-first input 785, an eighty-second input 790, an eighty-third input 795, an eighty-fourth input 800, an eighty-fifth input 805, an eighty-sixth input 810, an eighty-seventh input 815, an eighty-eighth input 820, an eighty-ninth input 825, a ninetieth input 830, a ninety-first input 835, a ninety-second input 840, a ninety-third input 845, a ninety-fourth input 850, a ninety-fifth input 855, a ninety-sixth input 860, a ninety-seventh input 865, a ninety-eighth input 870, a ninety-ninth input 875, a hundredth input 880, a hundred-first input 885, a hundred-second input 890, a hundred-third input 895, a hundred-fourth input 900, a hundred-fifth input 905, a hundred-sixth input 910, a hundred-seventh input 915, a hundred-eighth input 920, a hundred-ninth input 925, a hundred-tenth input 930, a hundred-eleventh input 935, a hundred-twelfth input 940, a hundred-thirteenth input 945, a hundred-fourteenth input 950, a hundred-fifteenth input 955, a hundred-sixteenth input 960, a hundred-seventeenth input 965, a hundred-eighteenth input 970, a hundred-nineteenth input 975, a hundred-twentieth input 980, a hundred-twenty-first input 985, a hundred-twenty-second input 990, a hundred-twenty-third input 995, a hundred-twenty-fourth input 1000.

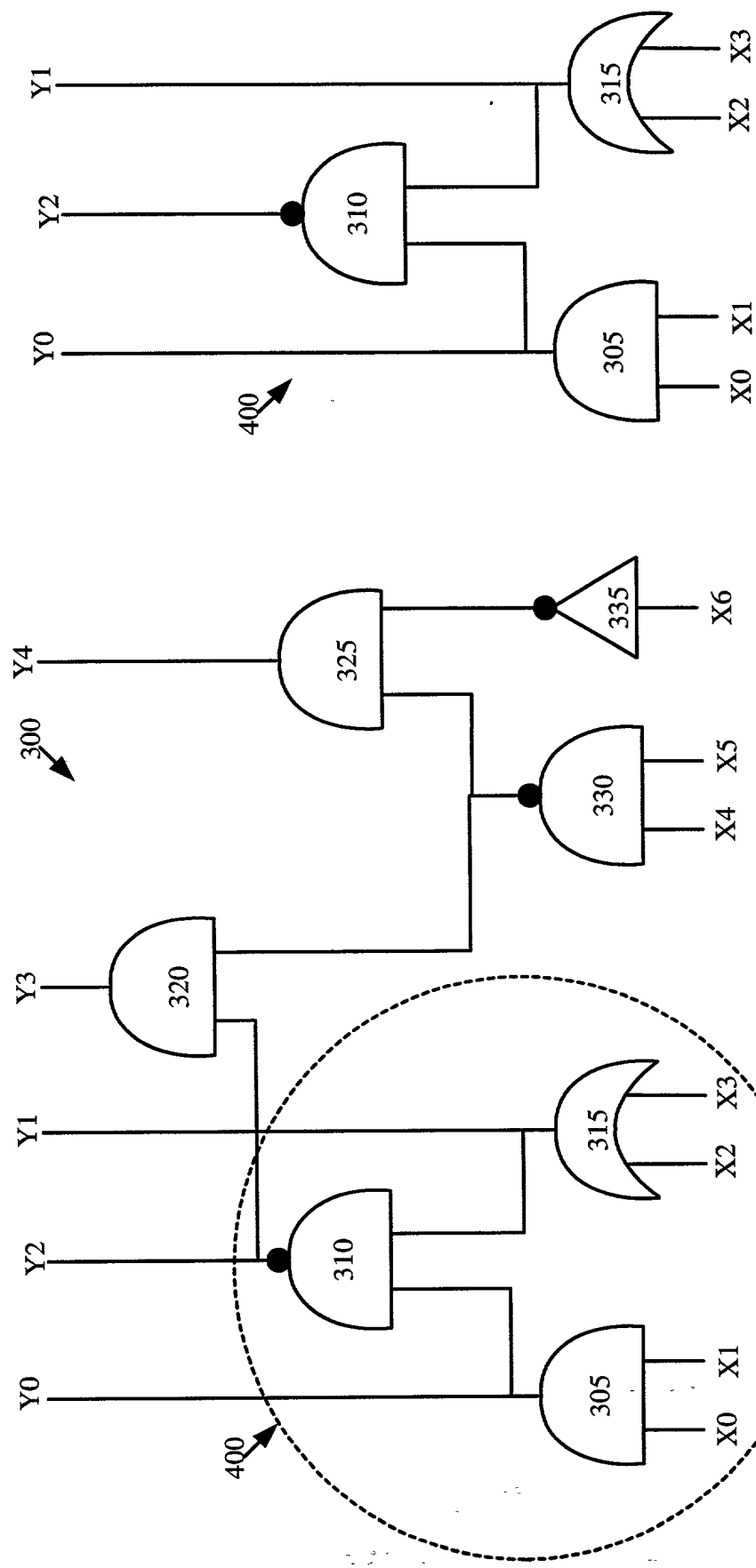


Figure 3

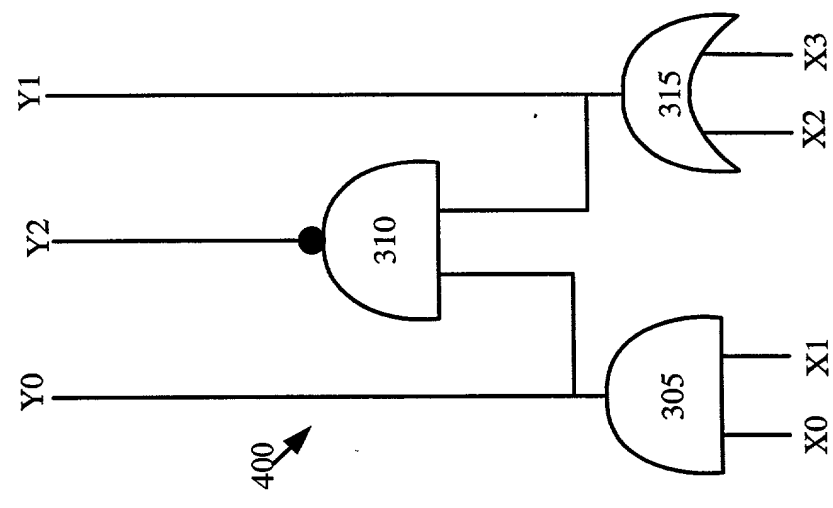


Figure 4

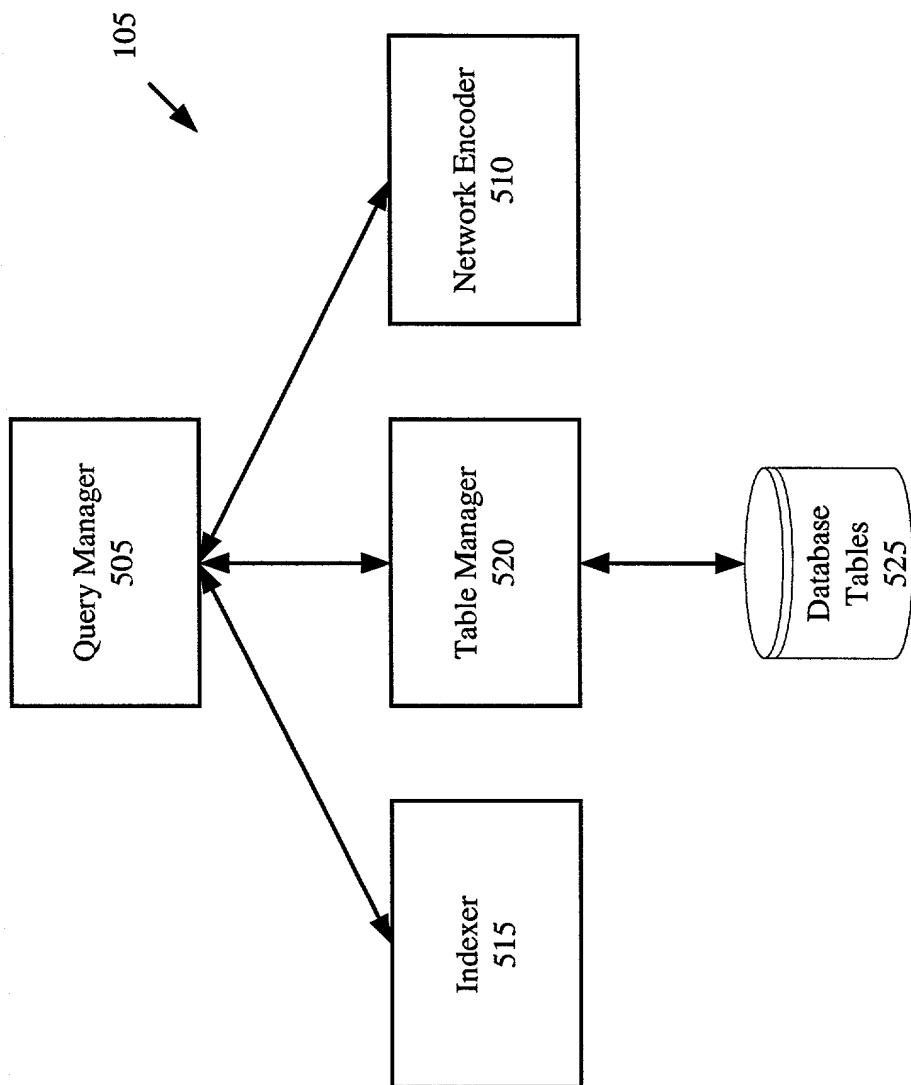


Figure 5

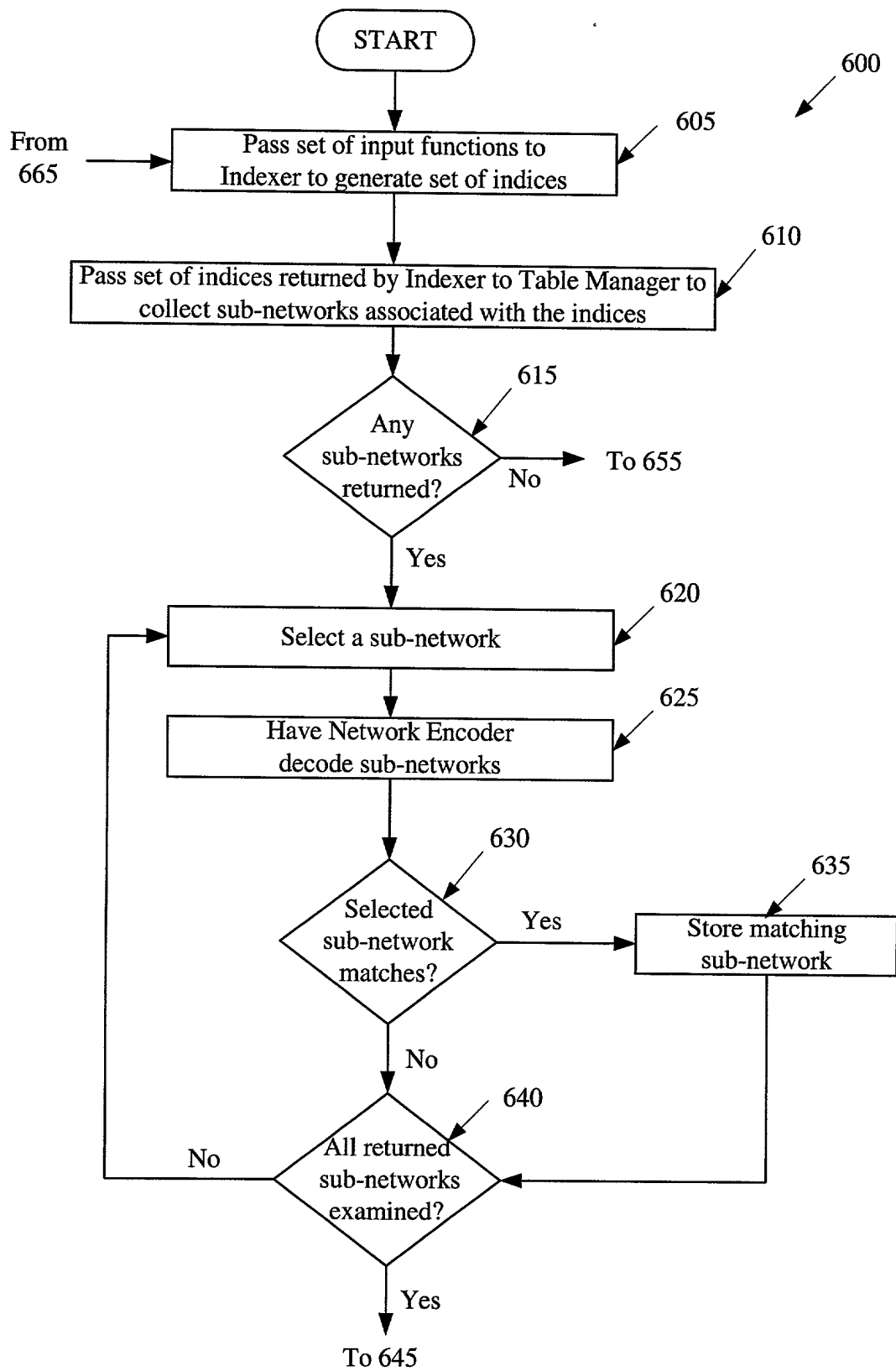


Figure 6A

Figure 6: Figure 6A
Figure 6B

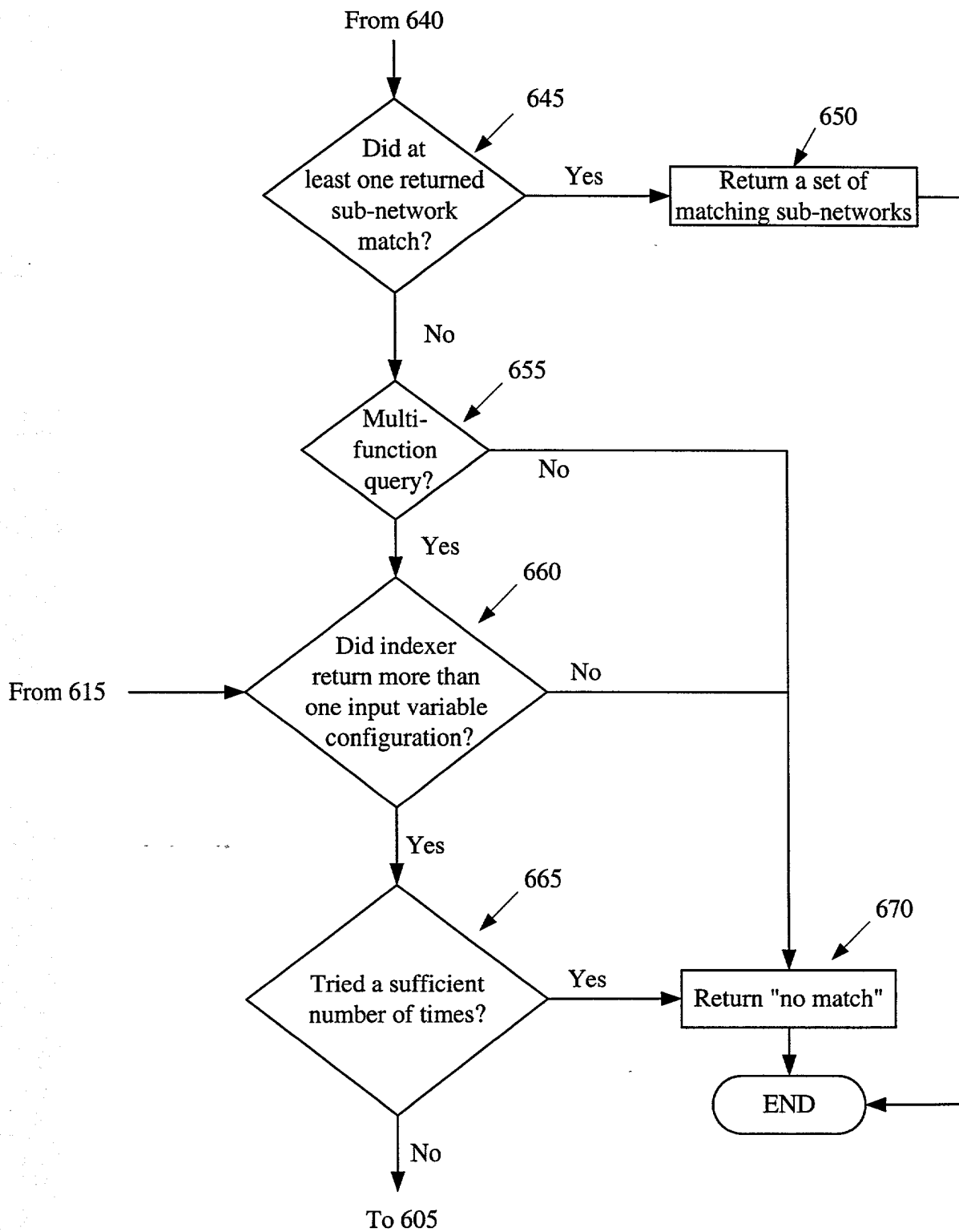


Figure 6B

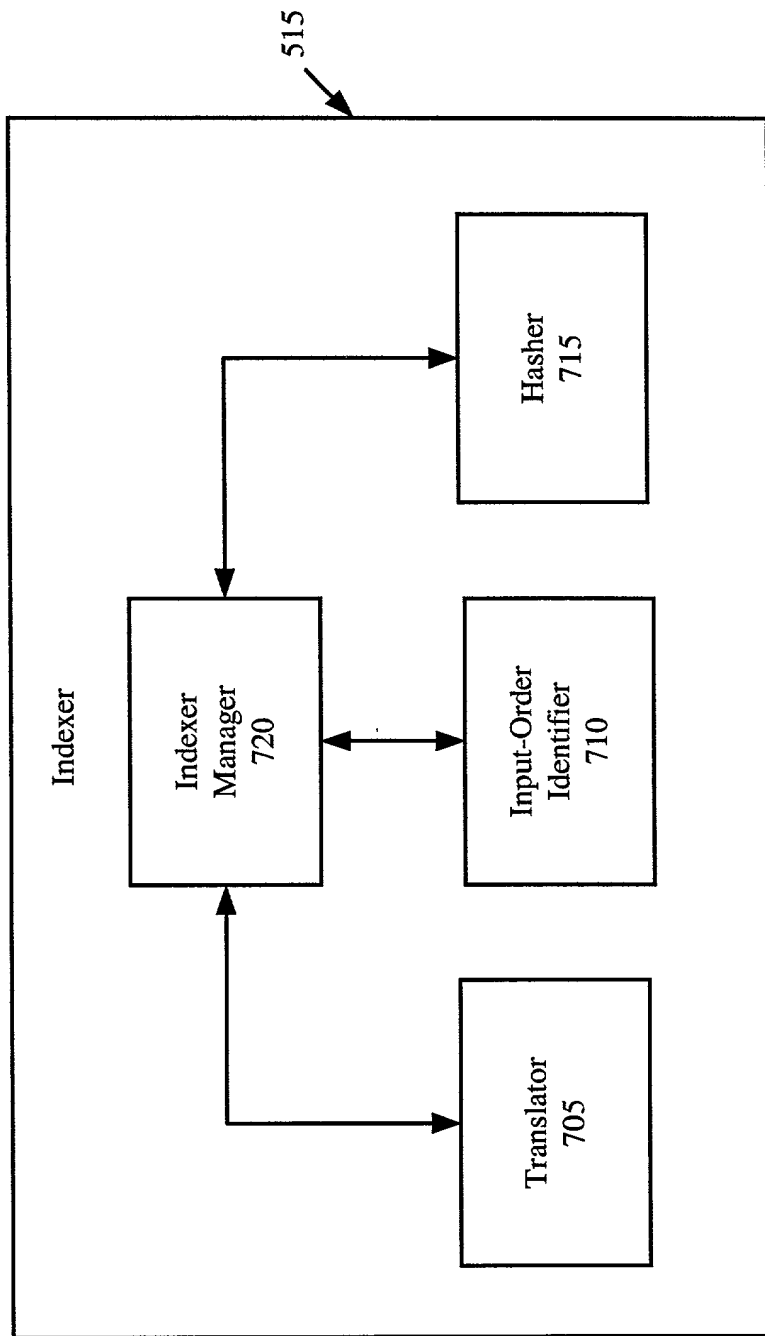


Figure 7

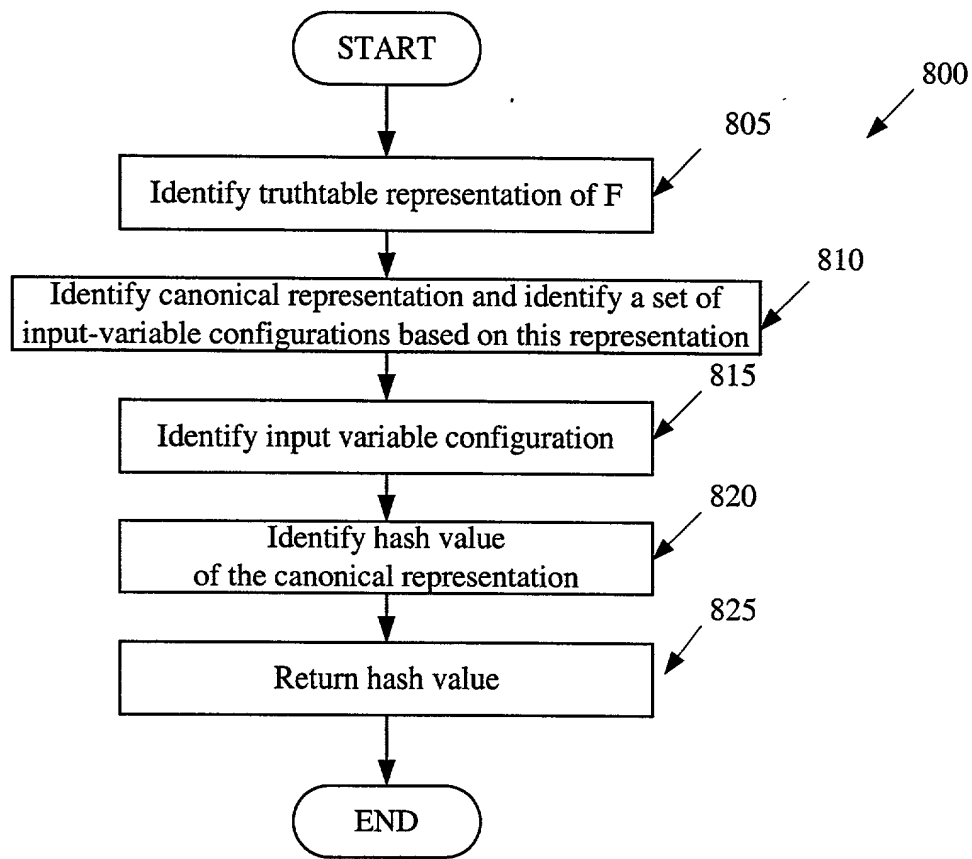


Figure 8

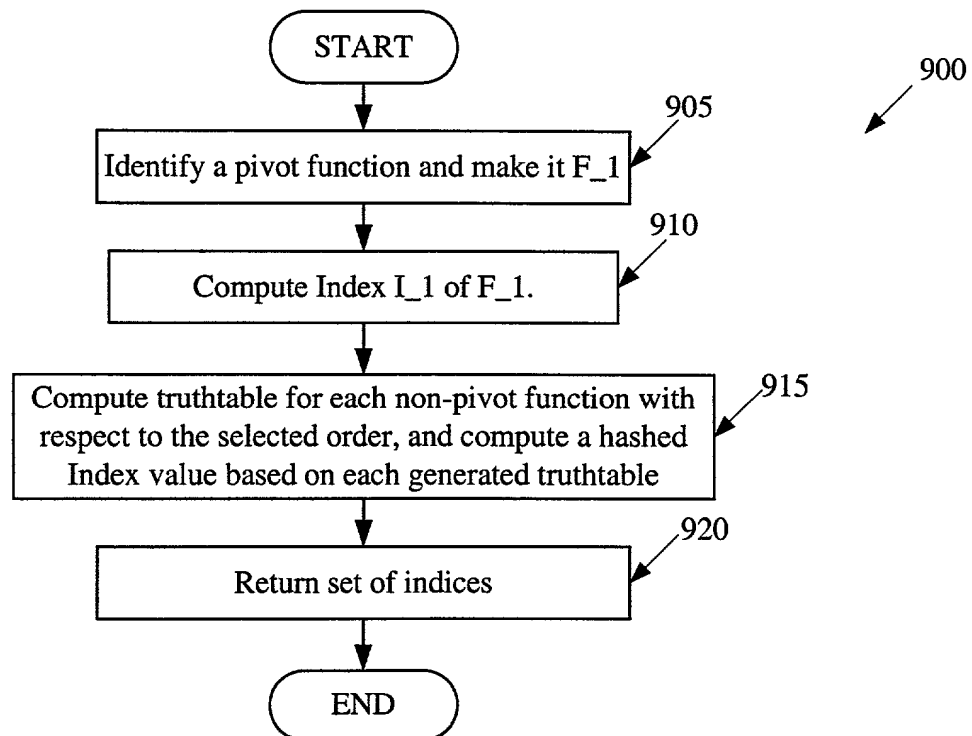


Figure 9

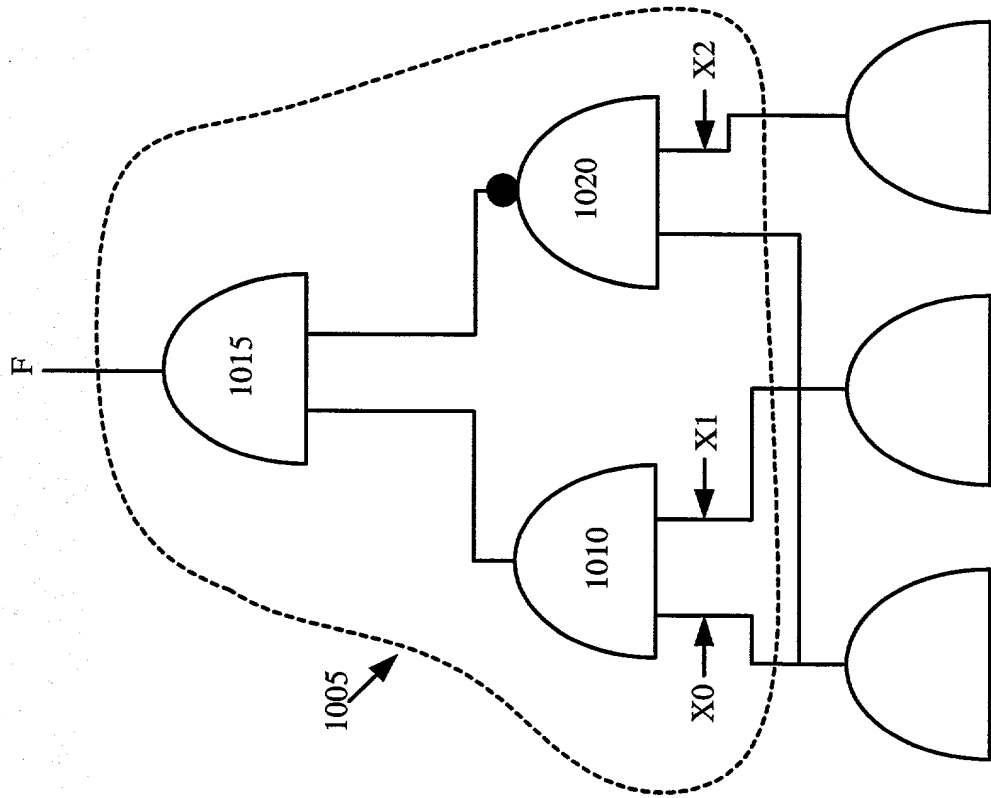
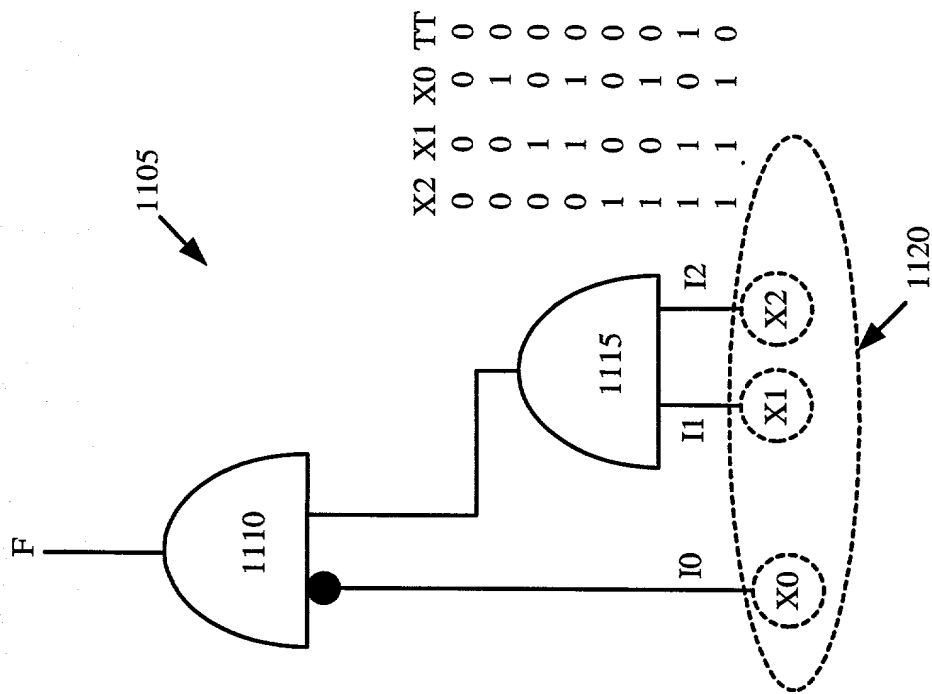
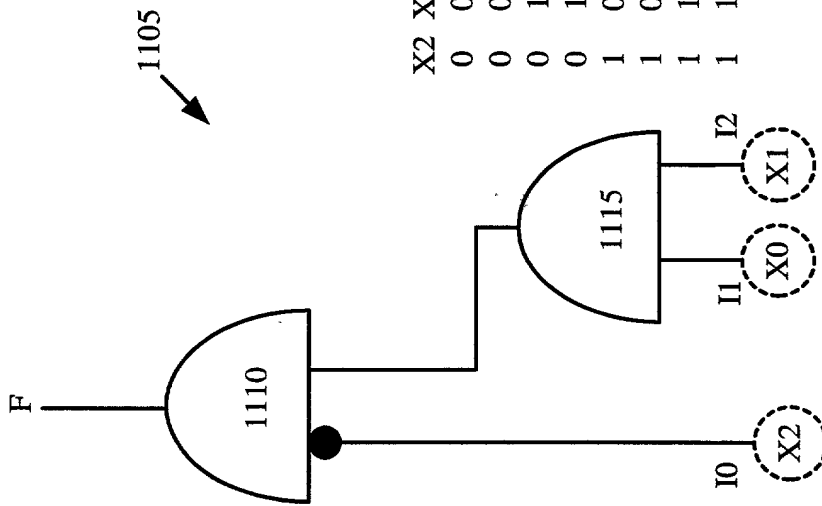


Figure 10



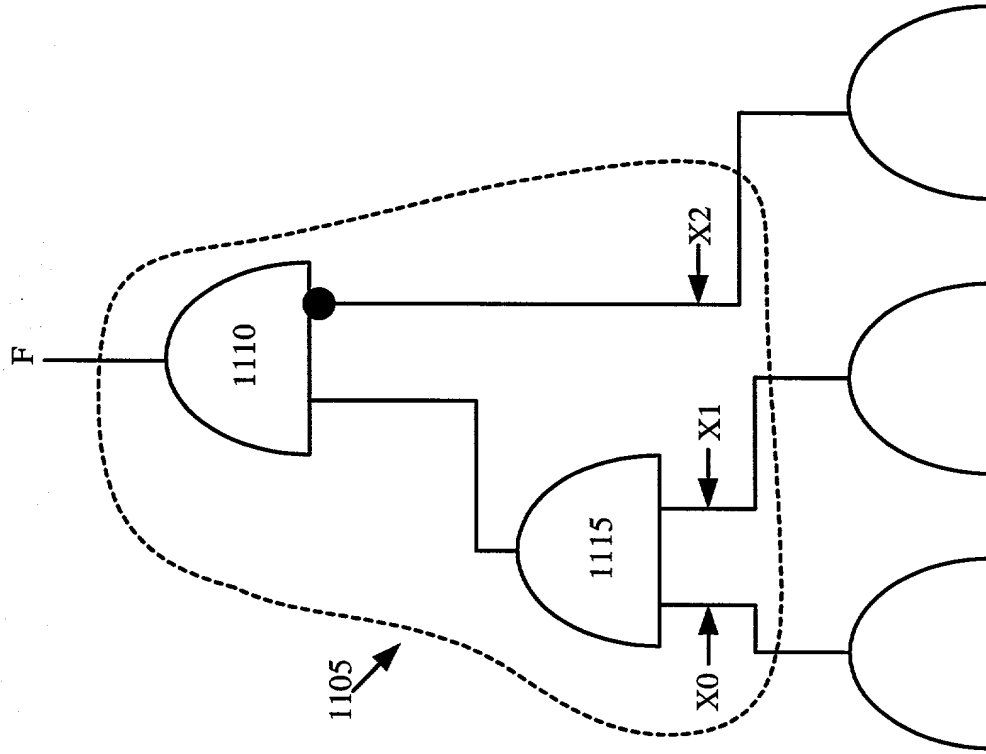
X2	X1	X0	TT
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	0

Figure 11



X2	X1	X0	TT
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	0

Figure 12



X2	X1	X0	TT
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	0

Figure 13

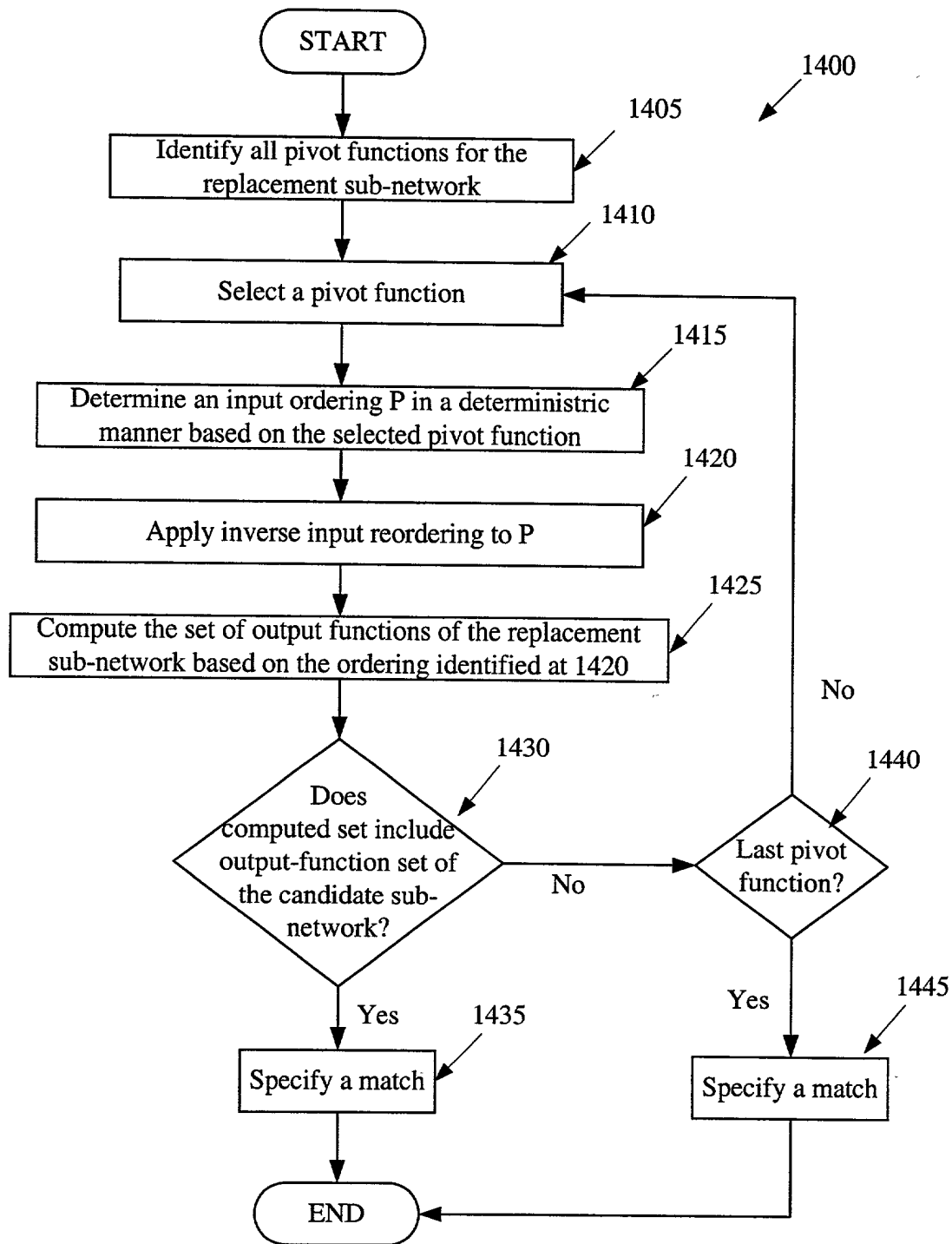


Figure 14

FIG. 15 is a block diagram of a network device 1500, which may be a switch, router, or other network device. The network device 1500 includes a processor 1505, a memory 1510, and a network interface 1515. The processor 1505 is configured to execute instructions stored in the memory 1510 to perform operations related to network management. The network interface 1515 is configured to communicate with other network devices over a network.

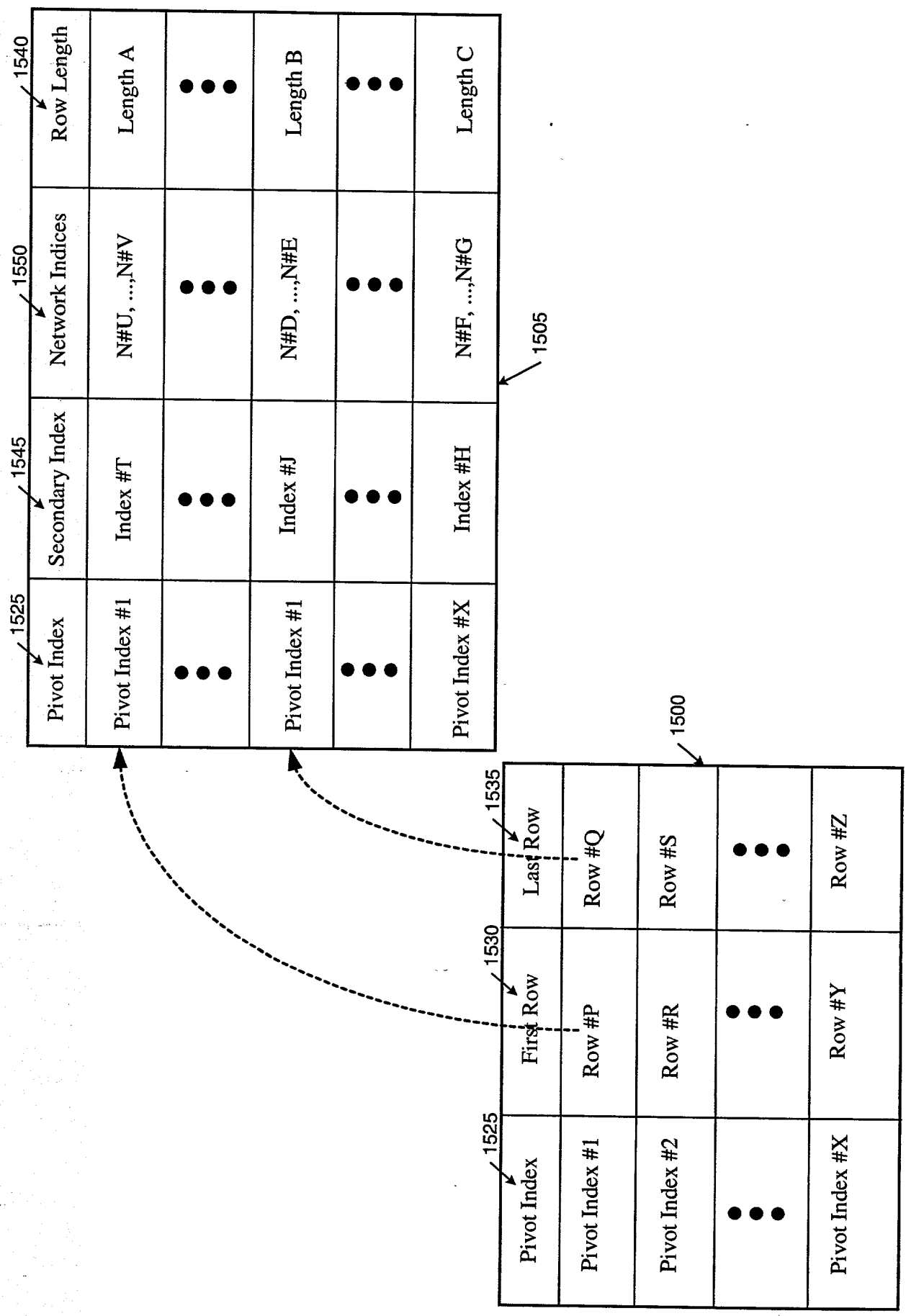


Figure 15

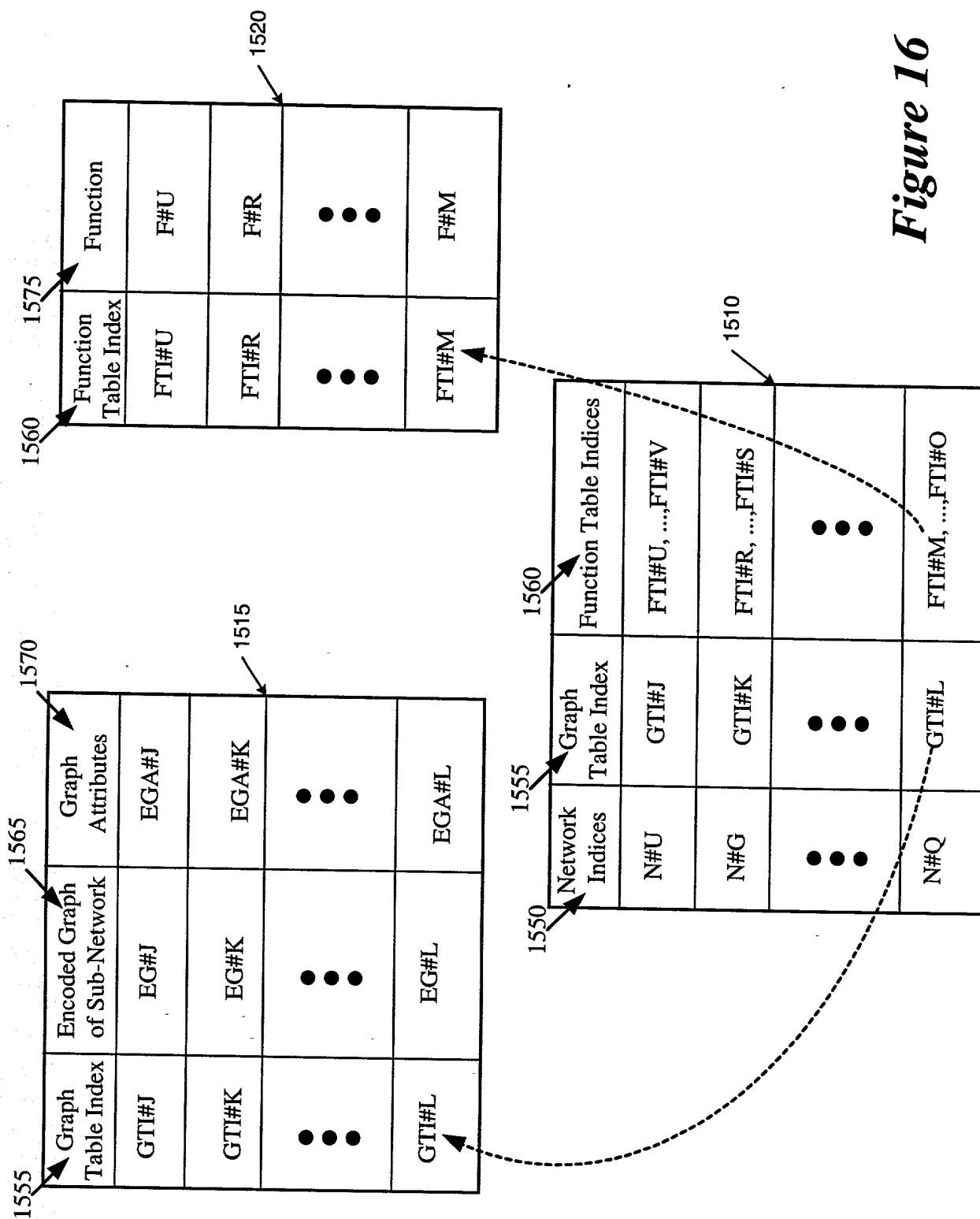


Figure 16

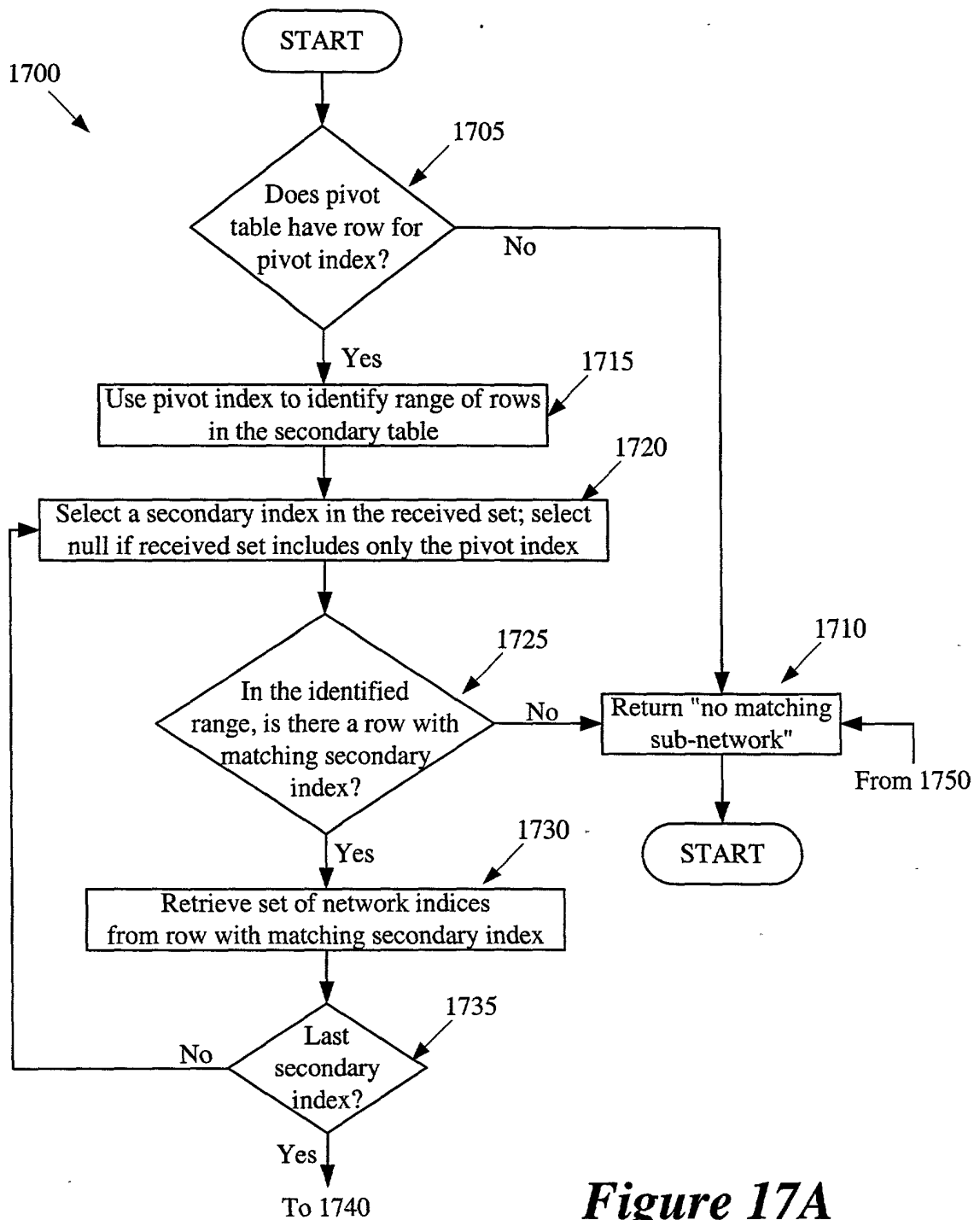


Figure 17A

Figure 17: Figure 17A
Figure 17B

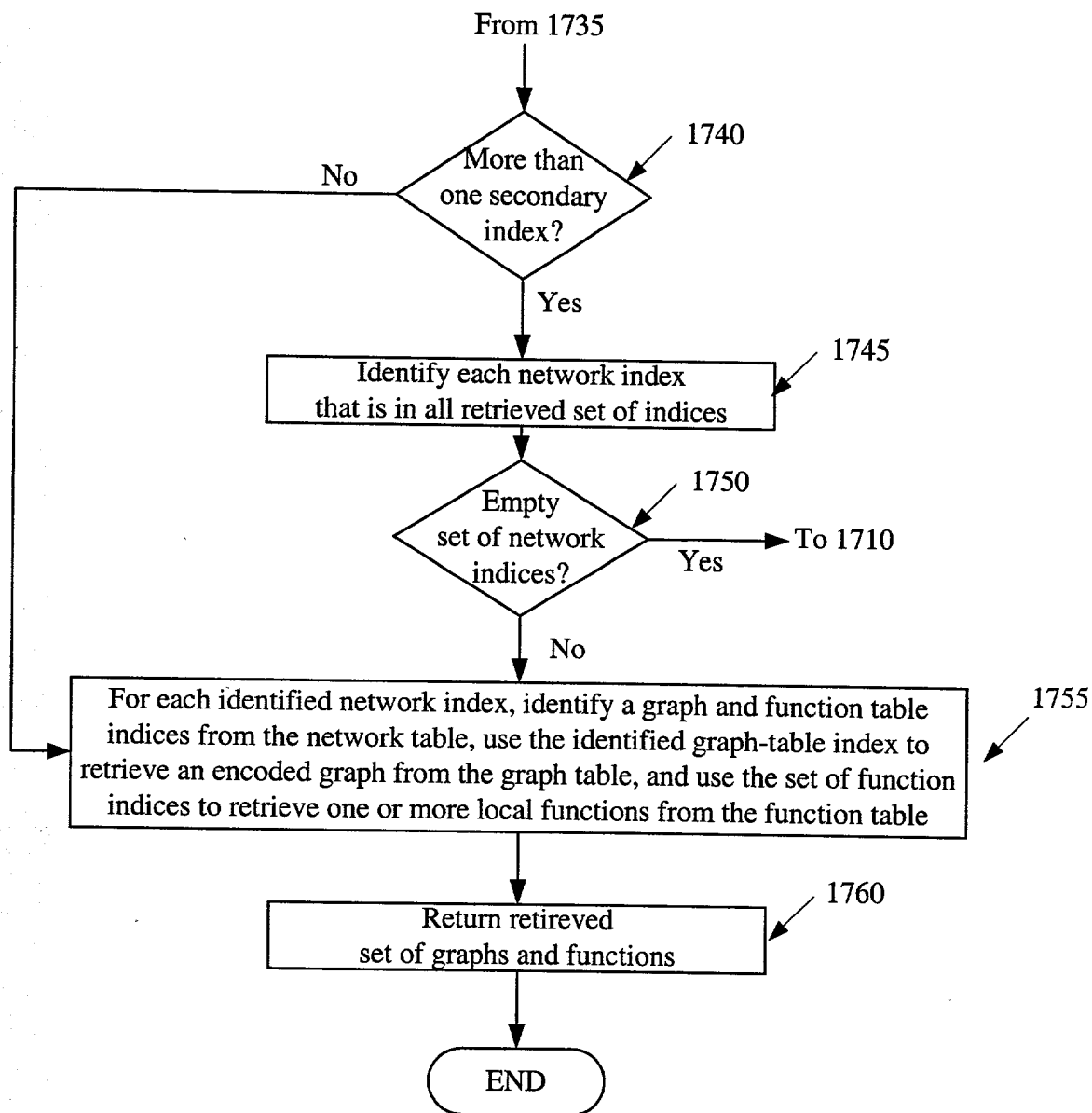


Figure 17B

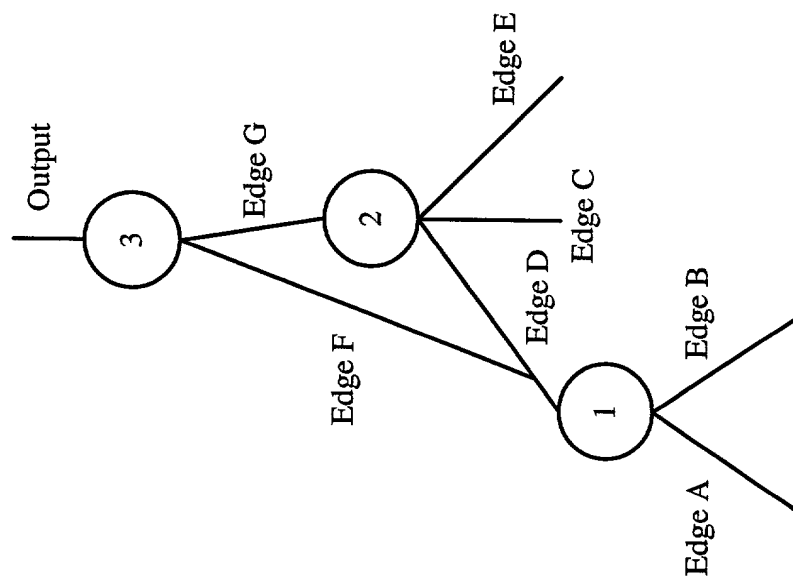


Figure 18

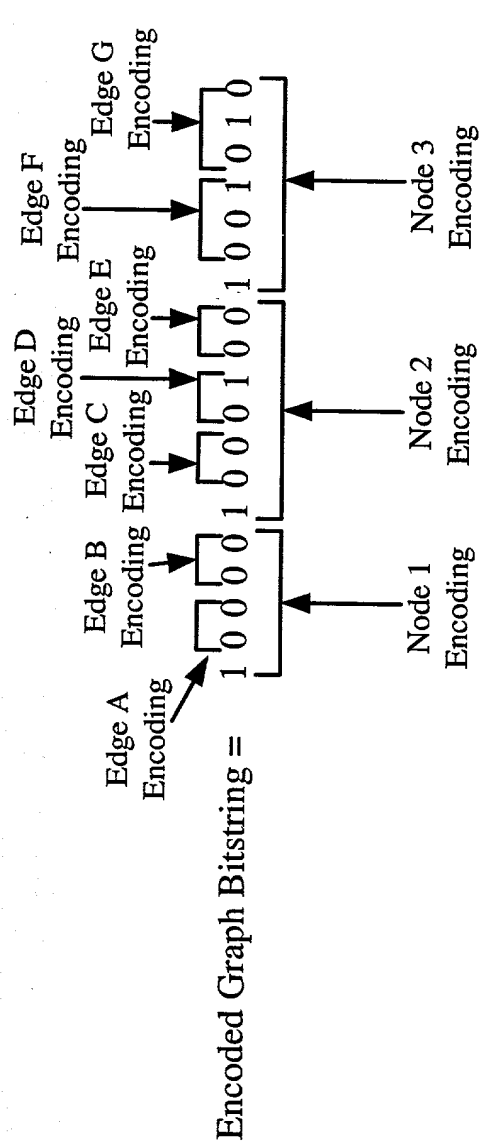


Figure 19

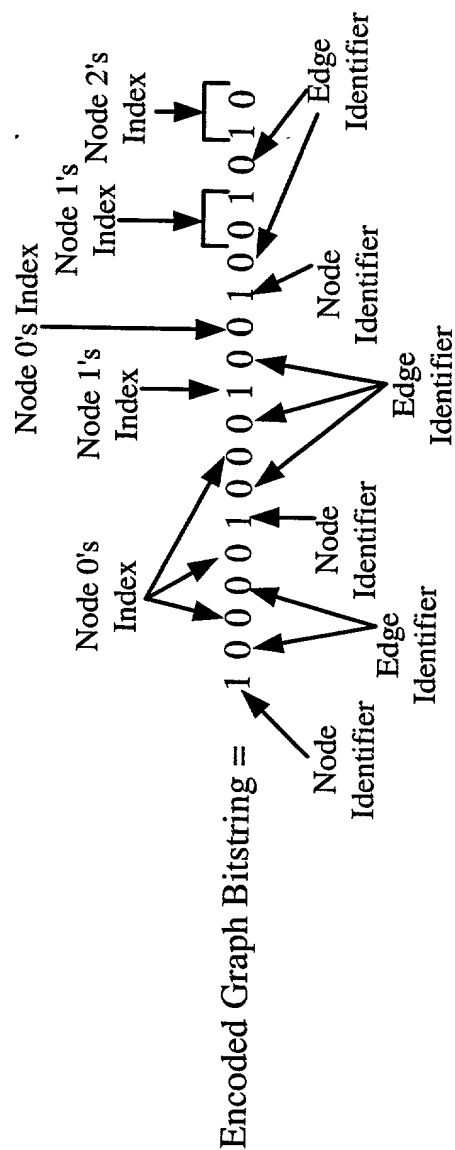


Figure 20

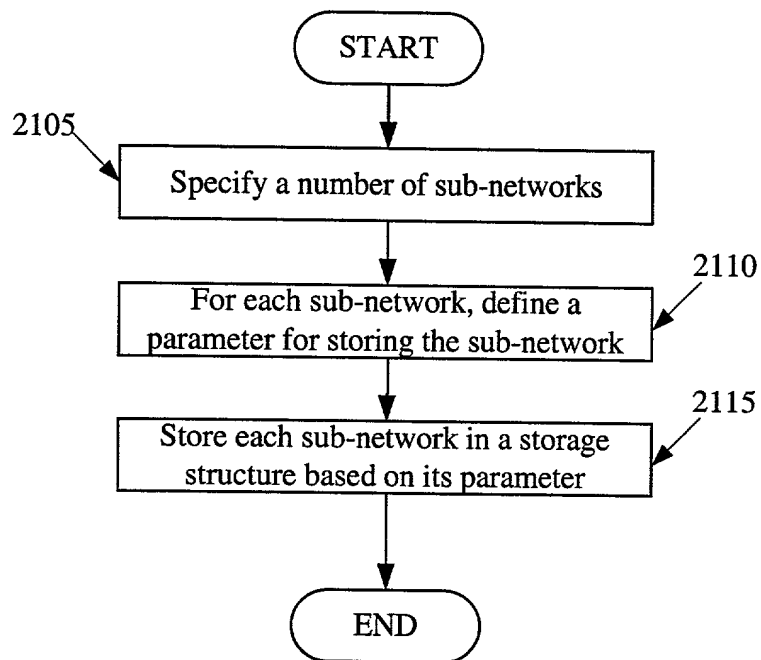


Figure 21

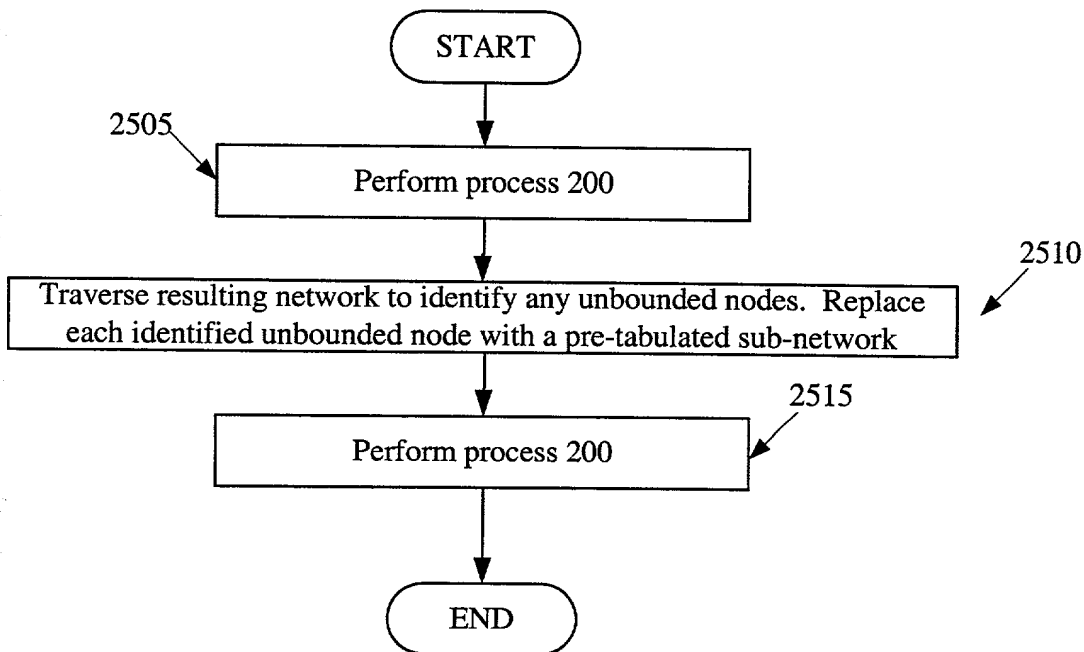


Figure 25

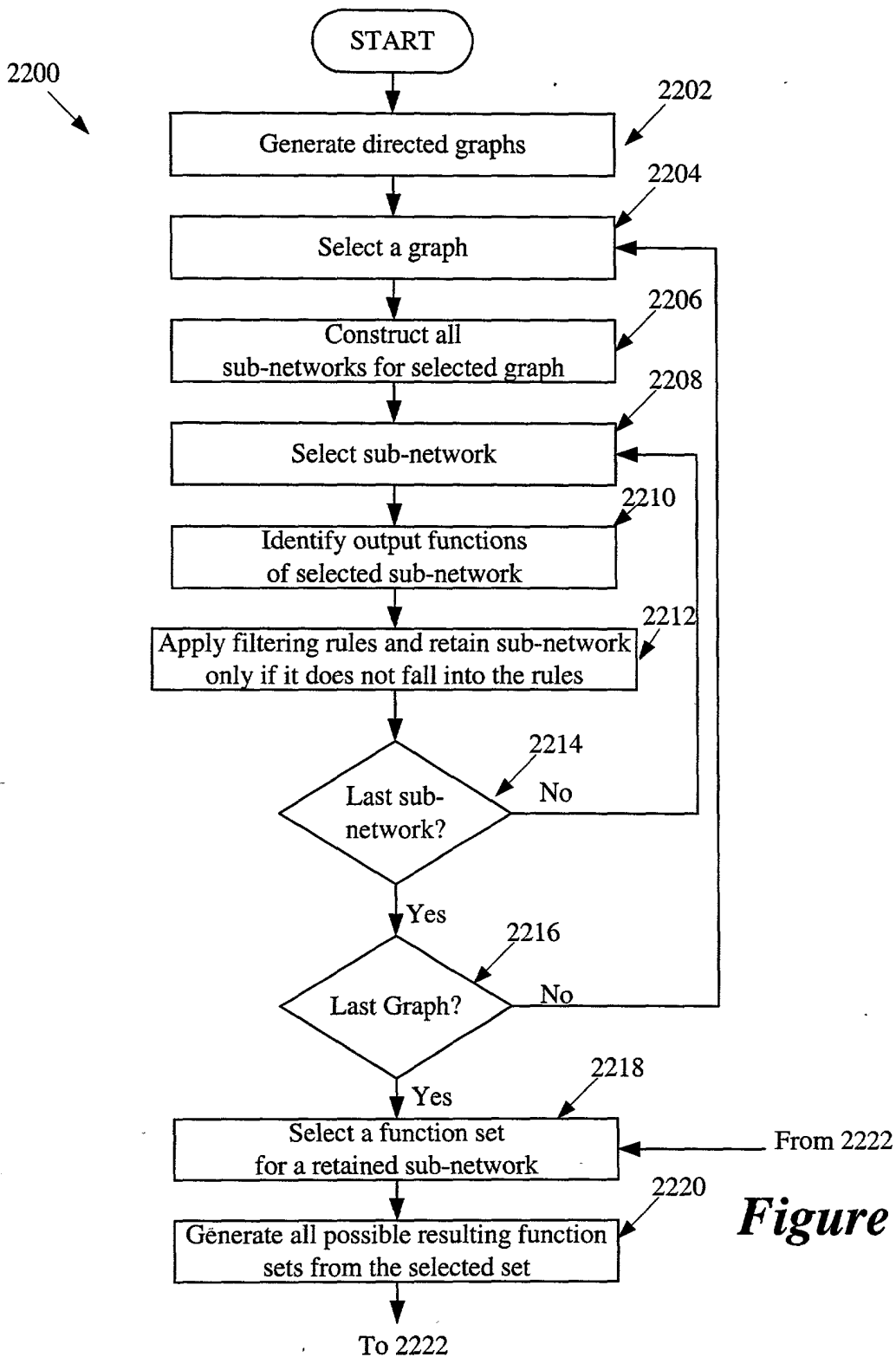


Figure 22: Figure 22A
Figure 22B
Figure 22C

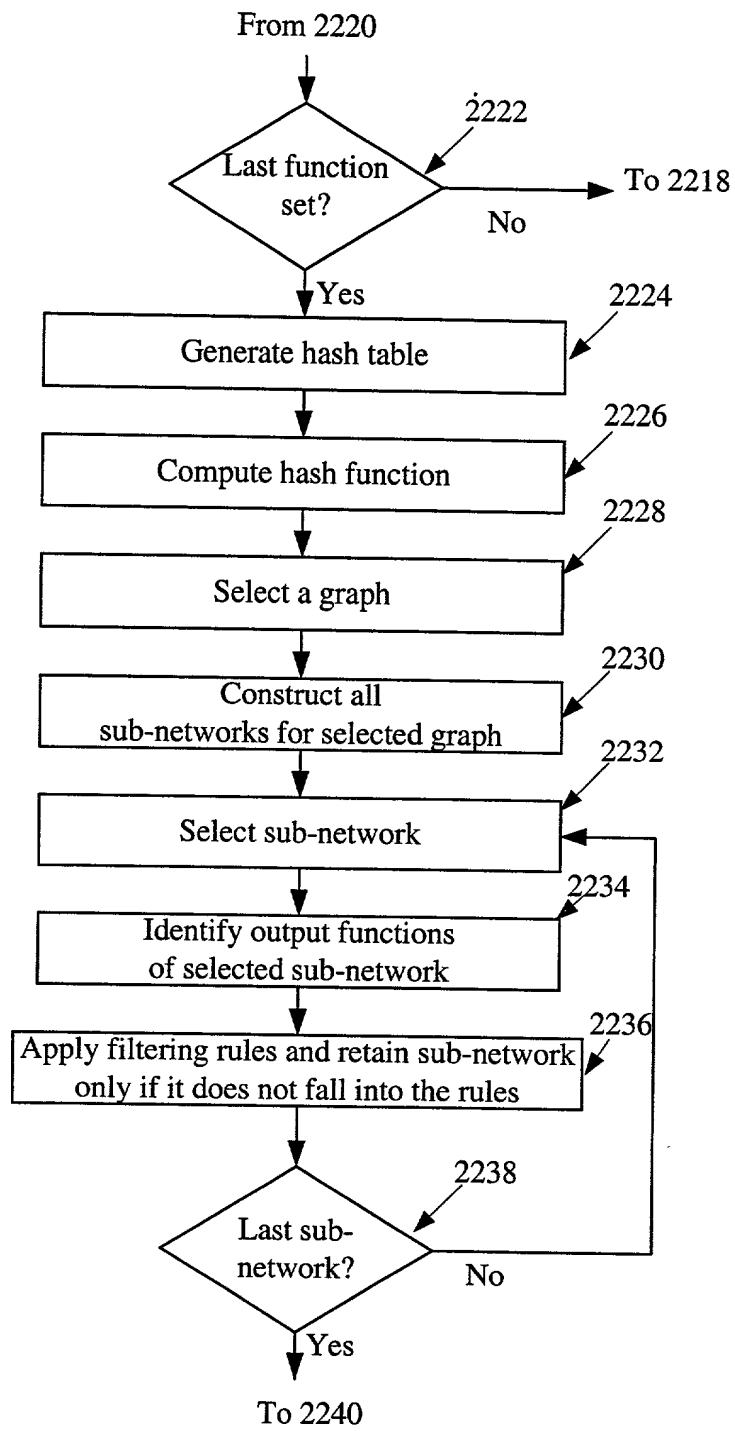


Figure 22B

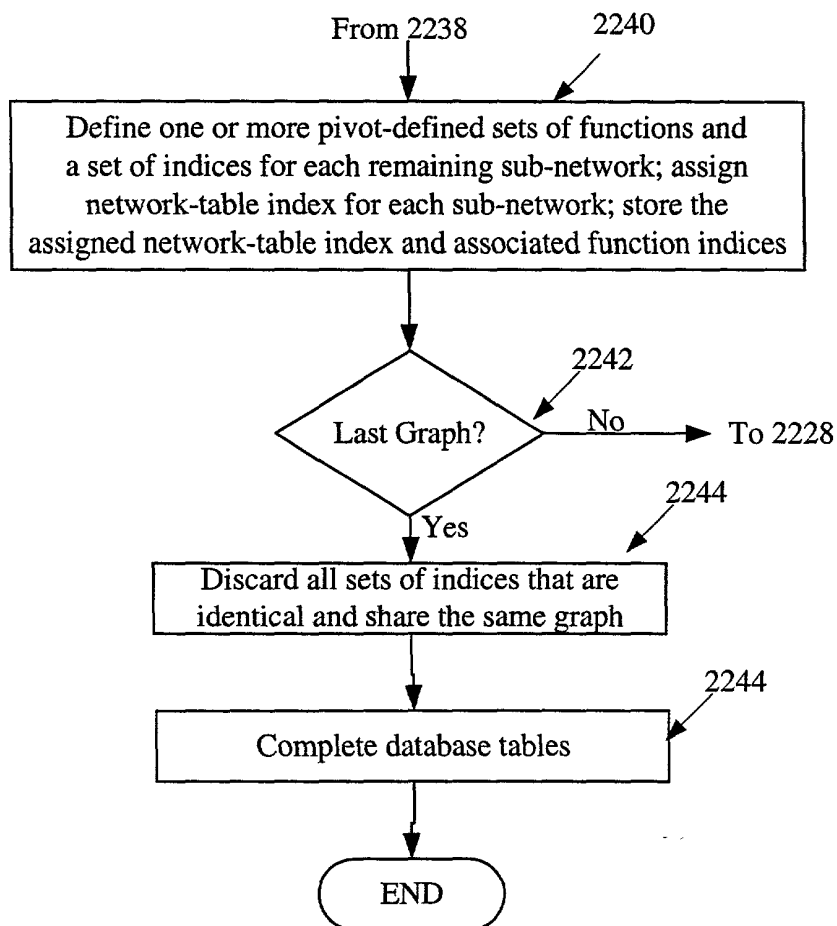


Figure 22C

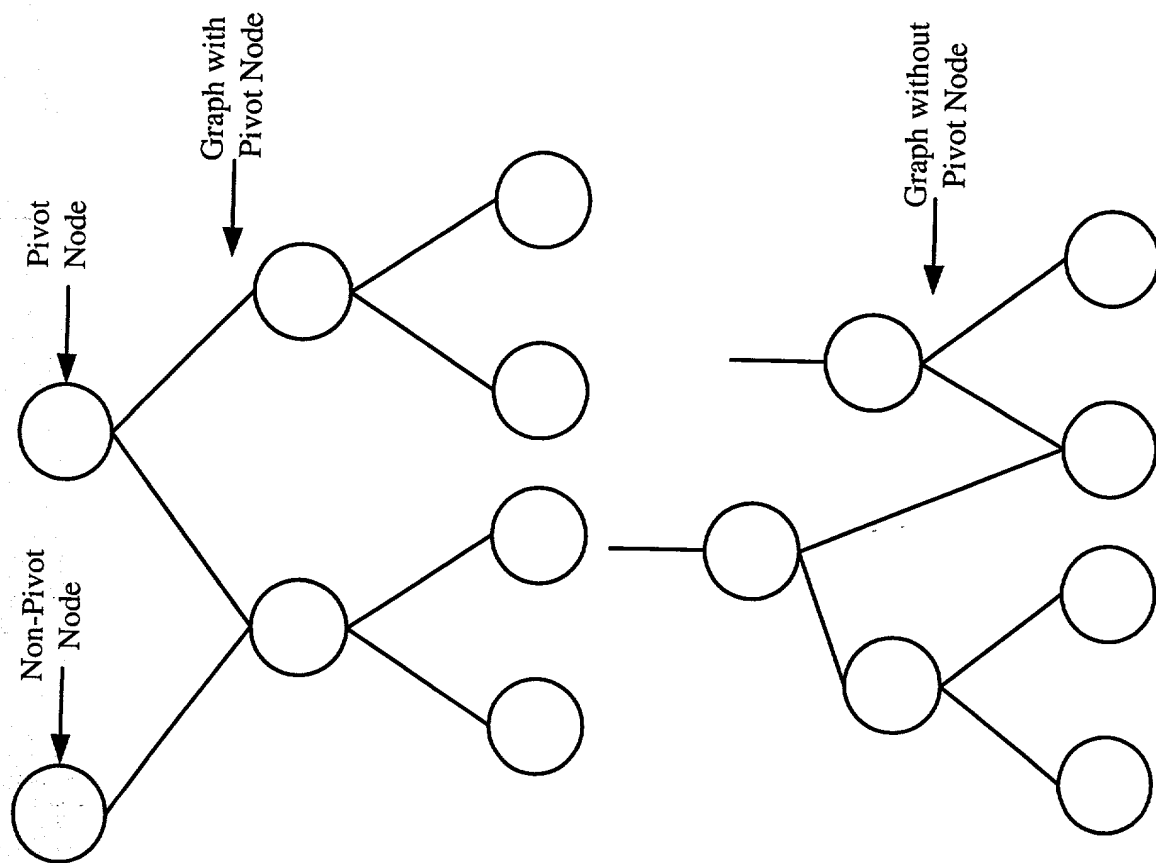


Figure 23

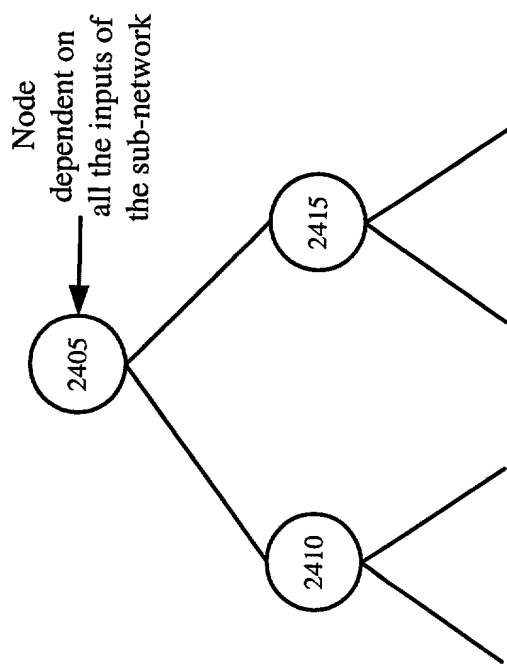


Figure 24

FIG. 26 is a block diagram of a system 2600. The system 2600 includes a ROM 2620, a processor 2610, input devices 2630, output devices 2635, system memory 2615, and a network 2665. The system 2600 is connected to a network 2665.

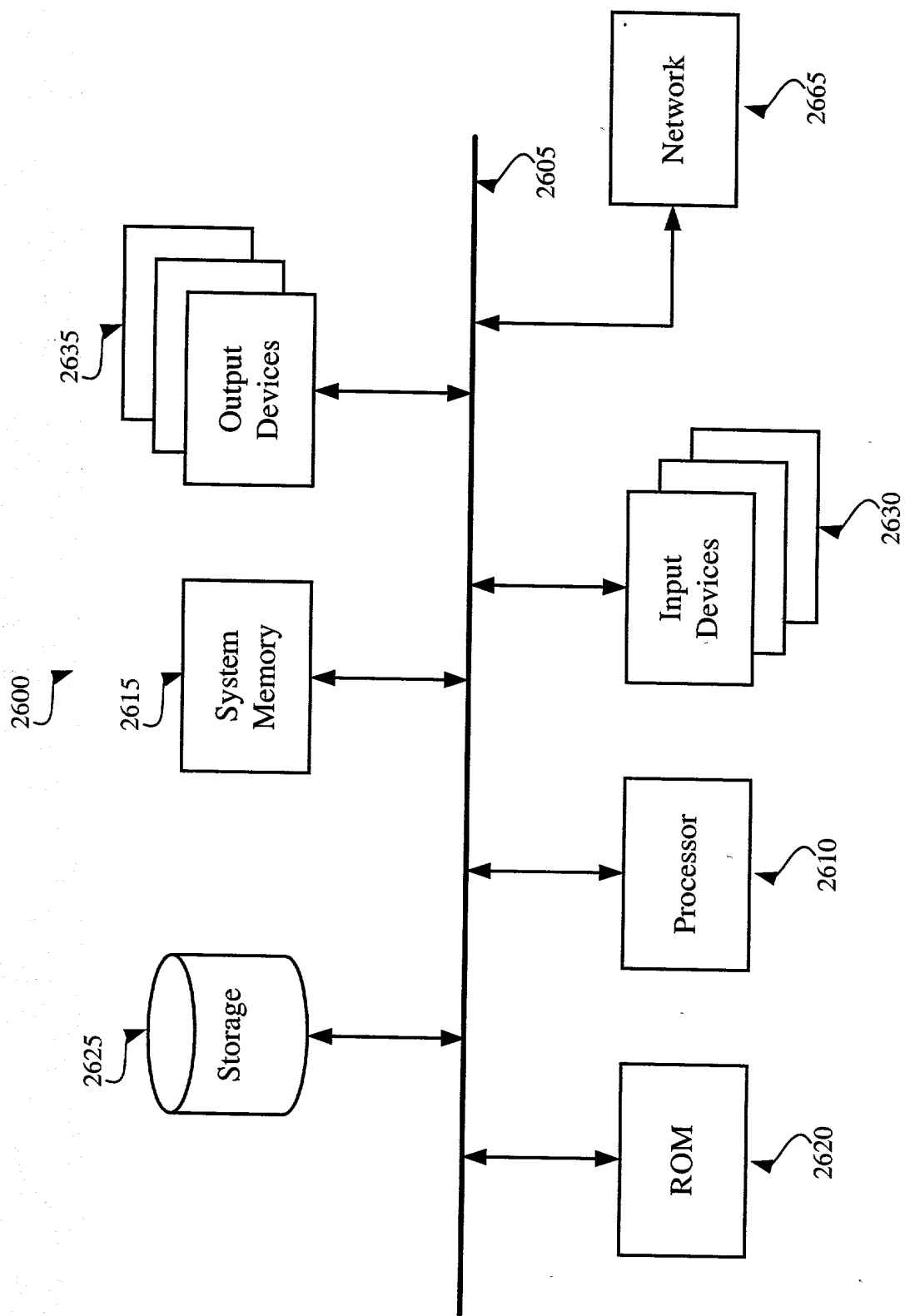


Figure 26